

ADDRESSING REFERENCE:

FUNC.	MAR	C.M.	L.M.	R0 BITS
YSK	0	7	10	LSB
YDR	1	8	11	
	2	9	12	
	3	10	13	
	4	11	14	
	5	12	15	
	6	13	16	
XSK	7	14	17	
	8	15	18	
	9	16	19	
XDR	10	17	20	
	11	18	21	

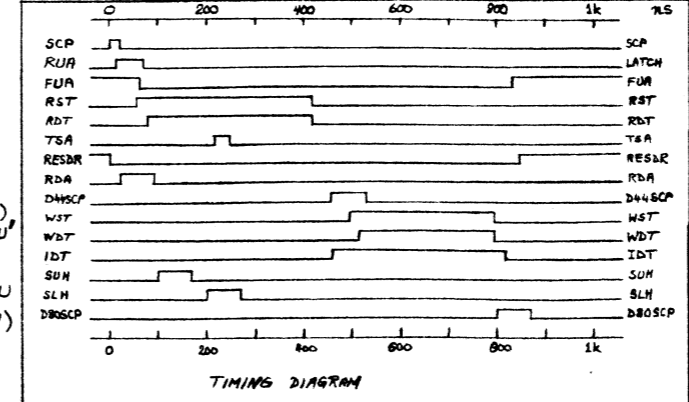
CENTRAL MEMORY:
 R0(20) : MODULE
 R0(21) } QUADRANT
 R0(22)

J4-29 MAR12 (R0(19), CM) LO: STACKA HI: STACKB
 C.M.: R0(19) = 0 R0(19) = 1
 L.M.: R0(22) = 1 R0(22) = 0

J4-31 (R0(22), LM) R0(23) = 0 R0(23) = 1
 HI: LOWER LO: UPPER

J4-46 RDU/L (R0(23)) READ DATA UPPER/LOWER HALF (DR 24-30) (DR 17-23)

* NEED VERIFICATION CARDS B25 & B26: U3 & U6 ARE SN74S40.



REVISIONS		DATE	DRAWN	APPROVED
NO.	DESCRIPTION			

DESIGNED BY: [Signature]
 DRAWN BY: [Signature]
 CHECKED BY: [Signature]
 DATE: 2420
 DRAWING NO: L00520.1

Berkeley Computer Corporation