

NOTE 1: COULDN'T CHECK PROPER JUMPER CONFIGURATION.  
HAD NO MVME101 PROCESSORS IN THE LAB.

**PART NUMBERS:**

MVME101 01-G3012M01 76431501 EURO BUILD

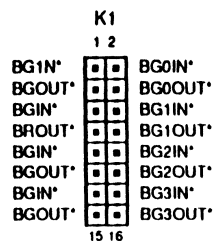
MVME101 01-W3529B01 76431501 US BUILD

SEE CURRENT REVISION LEVEL (CRL) FOR  
CURRENT REVISION INFORMATION.

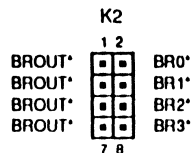
09/11/89

**MVME101  
VME module  
MONOBOARD  
MICROCOMPUTER  
PAGE 1 OF 2**

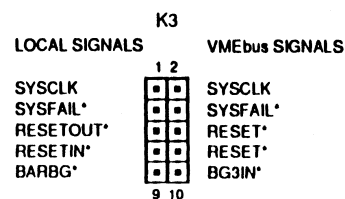
**VMEBUS GRANT SELECT**



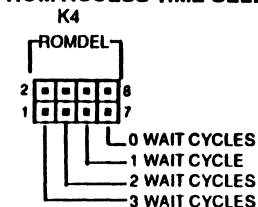
**BUS REQUEST SELECT**



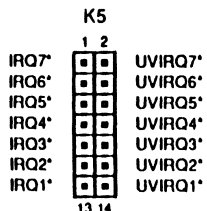
**VMEBUS SYSTEM CONTROL FUNCTION SELECT**



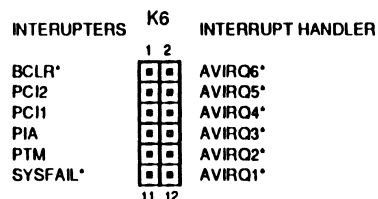
**LOCAL ROM ACCESS TIME SELECT**



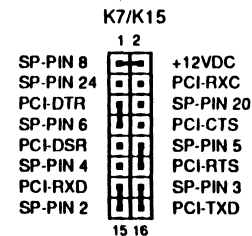
**USER-VECTORED INTERRUPT REQUEST LELECT**



**AUTO-VECTORED INTERRUPT REQUEST SELECT**

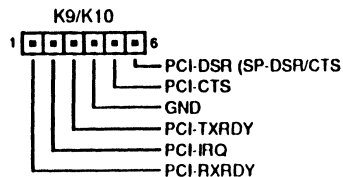


**SERIAL PORT CONFIGURATION SELECT**



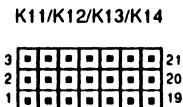
6-8 DSR/CTS CONTROLLED BY DTE  
 9-11 DSR/CTS CONTROLLED BY DTE  
 7-8 DSR/CTS CONTROLLED BY DCE  
 9-10 DSR/CTS CONTROLLEDE BY DCE  
 3-4 OPEN ASYNCHRONOUS DATA TRANSMISSION  
 3-4 JUMPERED SYNCHRONOUS DAT TRANSMISSION  
**THIS IS A DCE JUMPER CONFIGURATION.**

**SERIAL INTERFACE CONTROL SELECT**



- 1-2 INTERRUPT ACTIVATED BY RXRDY
- 2-3 INTERRUPT ACTIVATED BY TXRDY
- 1-2-3 INTERRUPT ACTIVATED BY RXRDY AND TXRDY
- 4-5 CTS INPUT CONSTANTLY ENABLED
- 5-6 CTS INPUT ENABLED BY PERIPHERAL DEVICE

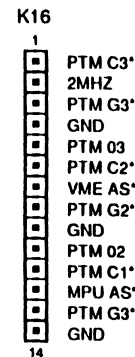
**MEMORY SOCKETS CONFIGURATION SELECT**



- K11 = U53 MEM1U EVEN BYTES  
U61 MEM1L ODD BYTES
- K12 = U54 MEM2U EVEN BYTES  
U62 MEM2L ODD BYTES
- K13 = U55 MEM3U EVEN BYTES  
U63 MEM3L ODD BYTES
- K14 = U56 MEM4U EVEN BYTES  
U64 MEM4L ODD BYTES

THIS IS TOO COMPLEX TO PUT ON THIS SHEET  
 REFER TO MANUAL FOR DETAILED EXPLANATIONS.

**PROGRAMMABLE TIME CONFIGURATION SELECT**



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