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1  *   GENERAL AUTOMATION, INC.  ALL RIGHTS RESERVED
2  *****
3  *
4  *   PROGRAM NAME   FPH-28
5  *
6  *   MODEL NUMBER  BF028
7  *
8  *   PURPOSE       FORTRAN PHASE-28
9  *
10 *   PROGRAMMER    DICK WALLMANN
11 *
12 *****   REVISION LIST   *****
13 *
14 *   RV DATE       SCO   BY   REASON FOR CHANGE
15 *   -- -- -- --  - - - - -
16 *
17 *   01 11/16/70  NONE  RPH  INITIAL RELEASE
18 *
19 *****
20 *****
21 HDNG   MPX FORTRAN ** RECOVERY - PHASE 28
22 *****
23 *
24 *STATUS - VERSION 1, MODIFICATION 0
25 *
26 *FUNCTION/OPERATION
27 *   * RECOVERY PHASE TERMINATES THE COMPILATION
28 *   OF THE SOURCE FORTRAN PROGRAM.
29 *   * THE STATUS OF THE COMPILATION IS PRINTED ON
30 *   BOTH THE LIST AND SYSTEM PRINTERS.  THE
31 *   MESSAGE END OF COMPILATION INDICATES A
32 *   SUCCESSFUL COMPILATION.  ALL OTHER MESSAGES
33 *   INDICATE AN UNSUCCESSFUL COMPILATION.
34 *
35 *ENTRY POINTS
36 *   * THE LABEL RECVR IS THE ONLY ENTRY
37 *
38 *INPUT
39 *   * THE CONTENTS OF RCODE IS THE ONLY INPUT.
40 *   PARTICULAR BIT PATTERNS ARE SET IN RCODE
41 *   BY PRECEEDING PHASES DEPENDING UPON THE
42 *   CURRENT STATUS OF THE COMPILATION.
43 *
44 *OUTPUT
45 *   * ONE OR MORE OF THE FOLLOWING MESSAGES
46 *   ON BOTH THE LIST AND SYSTEM PRINTERS
47 *   * WORKING STORAGE EXCEEDED
48 *   * END OF COMPILATION
49 *   * CARD READER OFF-LINE
50 *
51 *EXTERNAL REFERENCES
52 *   * SUBROUTINES - N/A
53 *   * COMMUNICATIONS AREAS - FCOM
54 *   * OTHER FORTRAN PHASES - NONE
55 *
56 ARS   REF CORE
57 *
58 *   MPX SYSTEM EQUATES
59 NORML EQU  /70   NORMAL EXIT

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60	ERROR	EQU	/70	ERROR EXIT
61	LIO	EQU	/85	IOCS ENTRY
62	*			
<del>63</del>	MEMRY	EQU	<del>4*320</del> CORE	MAXIMUM CORE SIZE
64	PHSIZ	EQU	4*320	MAXIMUM PHASE SIZE
65	OVERL	EQU	MEMRY-PHSIZ	PHASES 2-29 START
66	FCOM	EQU	OVERL-22	FORTRAN COMM. TABLE
67	PHNTB	EQU	FCOM-56	PHASE TABLE
68	ROLRX	EQU	PHNTB-50	INTERPHASE CALI
69	RCODE	EQU	FCOM	RECOVERY CODE
70	ERIND	EQU	FCOM+9	ERROR FLAG
71	*			
72		HDNG	MPX FORTRAN **	RECOVERY - PHASE 28
73		OPG	OVERL	
74	RECVR	LDX	I1 RCODE	XRI = RECOVERY CODE
75		BSC	I1 *	TO ROUTINE
76		DC	E0	NORMAL
77		DC	E1	WORK AREA EXCEEDED
78		DC	E0	*UNUSED
79		DC	E0	*UNUSED
80		DC	E4	SUBIN TOO LARGE
81		DC	E5	CORE REQ. EXCESSIVE
82	*			
83	E0	LDX	L1 M1	NORMAL COMPILATION
84		MDX	OUT	
85	E1	LDX	L1 M2	WORK AREA EXCEEDED
86		MDX	OUT	
87	E4	LDX	L1 M3	SUBIN TOO LARGE
88		MDX	OUT	
89	E5	LDX	L1 M4	CORE REQ. EXCESSIVE
90	*			
91	OUT	STX	I1 OUT1	STORE MESSAGE ADDR.
92		STX	I1 OUT2	
93		BST	L LIO	LIST MESSAGE
94		DC	/2005	
95	OUT1	DC	*-*	
96		DC	0	
97		BST	L LIO	WAIT FOR TRANSFER
98		DC	/F005	
99		BST	L LIO	LIST MESSAGE
100		DC	/2007	
101	OUT2	DC	*-*	
102		DC	0	
103		BST	L LIO	WAIT FOR TRANSFER
104		DC	/F007	
105		BST	L LIO	CLOSE BO FILE
106		DC	/B004	
107		DC	0	
108		DC	0	
109		BST	L LIO	
110		DC	/F004	
111		LD	L RCODE	
112		A	L ERIND	
113		BST	I OUT3-Z	ERROR OCCURRED
114		BST	L NORM	
115	OUT3	DC	ERROR	
116	*		MESSAGES TO OUTPUT	
117	M1	DC	M2-*	END OF COMPILATION
118		DC	/C5CE	EN
119		DC	/C4AC	D

120		DC	/CFC6	OF
121		DC	/A0C3	C
122		DC	/CFCD	OM
123		DC	/D0C9	PI
124		DC	/CCCJ	LA
125		DC	/D4C9	TI
126		DC	/CFCE	ON
127	M2	DC	M3-*	WORKING STORAGE EXCEE
128		DC	/D7CF	WO
129		DC	/D2CB	RK
130		DC	/C9CE	IN
131		DC	/C7A0	G
132		DC	/D3D4	ST
133		DC	/CFD2	OR
134		DC	/C1C7	AG
135		DC	/C5A0	E
136		DC	/C5D8	EX
137		DC	/C3C5	CE
138		DC	/C5C4	ED
139		DC	/C5C4	ED
140	M3	DC	M4-*	SUBIN TOO LARGE
141		DC	/D3D5	SU
142		DC	/C2C9	BI
143		DC	/CEA0	N
144		DC	/D4CF	TO
145		DC	/CFA0	D
146		DC	/CCCJ	LA
147		DC	/D2C7	RG
148		DC	/C5A0	E
149	M4	DC	M5-*	CORE REQUIREMENTS EXC
150		DC	/C3CF	CO
151		DC	/D2C5	RE
152		DC	/A0D2	R
153		DC	/C5D1	EQ
154		DC	/D5C9	UI
155		DC	/D2C5	RE
156		DC	/CDC5	ME
157		DC	/CED4	NT
158		DC	/D3A0	S
159		DC	/C5D4	EX
160		DC	/C3C5	CE
161		DC	/D3D3	SS
162		DC	/C9D6	IV
163		DC	/C5A0	E
164	M5	DC	*	
165		BSS	OVERL-#+320*2	PHASE-28 PATCH AREA
166		END	RECVB	